

SSC-JE

2025

Staff Selection Commission
Junior Engineer Examination

Electrical Engineering

Basic Electronics

Well Illustrated **Theory** *with*
Solved Examples and **Practice Questions**



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Basic Electronics

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Chapter 1

Semiconductor Physics

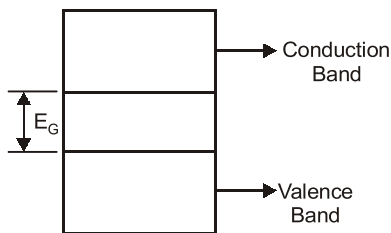
1.1 Introduction

A material whose conductivity lies between the metal and insulator is known as semiconductor.

Example : Silicon, Germanium

Energy Gap (E_g) or Band Gap

- The minimum energy required to detach an electron from valence band to conduction band is equal to its Energy Gap (E_g).



- Energy band gap E_g is a function of temperature and tends to decrease as the temperature increases.
- At any temperature T ,

$$E_g(T) = E_{g0} - \beta T$$

E_{g0} = Energy Band Gap at 0°K

β = Material constant (Unit : eV/ $^\circ\text{K}$)

Energy band structure of an insulator, a semiconductor and a metal

Insulator

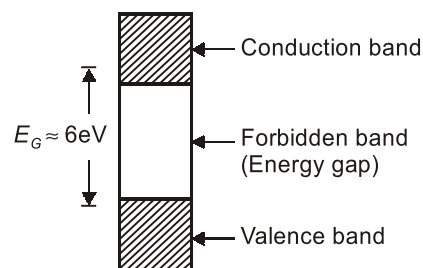
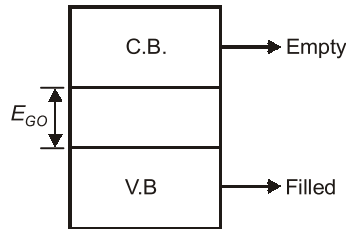


Fig. (a)

Example : Diamond

- Large forbidden band separates the filled valence band from the vacant conduction band. So electron cannot acquire sufficient applied energy hence conduction is impossible. All insulators are bad conductors of current at any temperature.

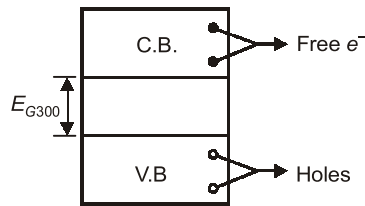
Semiconductor



Semiconductor at 0°K

Fig. (b)

- At 0°K, semiconductor behave as insulator because at 0°K conduction band is empty and there is no charge carrier in conduction band.



Semiconductor at 300k

- At 300 K free charge carriers are available, so semiconductor will contribute some current at 300 K.
- A material for which the width of forbidden energy band is relatively small (~1 eV) is called semiconductor

$$E_{G300} = 0.72 \text{ eV for Ge}$$

$$E_{G300} = 1.1 \text{ eV for Si}$$

$$E_G = 0.785 \text{ eV for Ge \& 1.21 eV for Si at } 0^\circ \text{ K}$$

NOTE:

Conduction in semiconductors does not obey ohm's law and will increase rapidly than voltage, i.e. semiconductors are non-linear resistors.

Metal

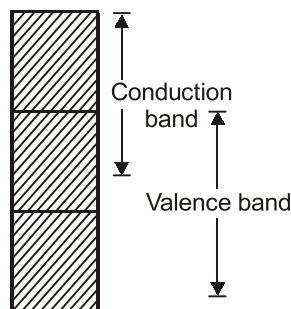


Fig. (c)

- Metals have overlapping valence and conduction bands.
- Metal are good conductor of current even at 0°K.
[Example : Copper (Cu) and Aluminium (Al)]

1.2 Transport Phenomena in Semiconductors

Mobility

- Mobility is the proportionality constant which essentially gauges how easily current carriers (i.e. electrons or holes) can move through a piece of semiconductor.
- When an electric field (E) is applied to a conductor or semiconductor then the charge carriers (electrons or holes) starts flowing in it with velocity known as drift velocity (v).
- Mobility is the proportionality constant that relates the drift velocity to the electric field strength in a semiconductor.

Hence, mobility of a carrier is given by

$$\mu = \frac{v}{|E|} \quad \dots(1.1)$$

- The unit of mobility is $m^2/volt\text{-}sec$.
- Electron mobility is always higher than hole mobility. For a particular material electron can travel faster and contribute more current than hole.

Conductivity

- The conductivity of a material can be related to the number of charge carriers present in the material. In case of conductors the conductivity is given as:

$$\sigma = n e \mu \quad \dots(1.2)$$

where,

n = number of free charge carriers

e = charge of e^-
= 1.602×10^{-19} col.

μ = mobility of e^-

σ = conductivity

- In semiconductor there are two types of charge carriers, i.e. electron and holes, hence the resultant conductivity of a semiconductor depend upon:
 - ♦ The concentration of the mobile charge carriers (i.e. electrons and holes) and
 - ♦ The mobility of the charge carriers.

\therefore conductivity due to electrons

$$\sigma_n = n e \mu_n$$

and conductivity due to holes

$$\sigma_p = p e \mu_p$$

Where, μ_n and μ_p are the mobility of electrons and holes respectively.

Total conductivity of a semiconductor.

$$\begin{aligned} \sigma &= \sigma_n + \sigma_p \\ &= n e \mu_n + p e \mu_p \end{aligned} \quad \dots(1.3)$$

- In case of intrinsic (Pure) semiconductors the electrons and holes are always present in equal concentration

$$n = p = n_i$$

Therefore, conductivity of intrinsic semiconductors.

$$\sigma_i = n_i e (\mu_p + \mu_n) \quad \dots(1.4)$$

Where, $n_i = n = p$ and is called intrinsic concentration.

Intrinsic resistivity is given as:

$$\rho_i = \frac{1}{\sigma_i} \quad \dots(1.5)$$

The unit of resistivity is ohm-m.

For good conductor, $n \cong 10^{28}$ electrons/m³

For insulator, $n \cong 10^7$ electrons/m³

For semiconductor the concentration of mobile charge carriers lies between these two values.

Current Density

$$\begin{aligned} J &= nev \\ &= \rho v \\ &= ne \mu E \end{aligned}$$

...(1.6)

$J \rightarrow$ current density (current / area)

$n \rightarrow$ carrier concentration (no. of carriers / m³)

$v \rightarrow$ velocity in meter per second

$\rho \rightarrow ne$ (charge density in coulombs / m³)

- For metals, $J = ne\mu E$
- For semiconductors, $J = ne\mu_n E + pe\mu_p E$
 $J = (n\mu_n + p\mu_p)eE$

Intrinsic Semiconductors

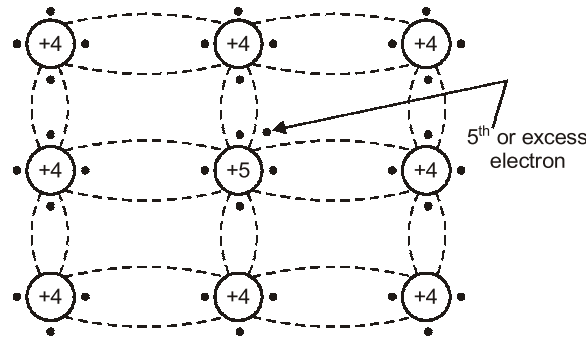
- At absolute zero, all the valence electrons are tightly bound to the parent atoms. No free electrons are available for conduction.
- The intrinsic semiconductor behaves as a perfect insulator at absolute zero.
- At room temperature, sufficient thermal energy is supplied to break the covalent bond, thereby, generating electron-hole pairs.
- The concentration of free electrons and holes will always be equal in an intrinsic semiconductor.

$$n = p = n_i \quad (n_i = \text{intrinsic concentration})$$

Extrinsic Semiconductors

- In addition to the intrinsic carriers generated thermally, it is possible to create carriers in semiconductors by introducing impurities into the crystal.
- This process, called doping, is the most common technique for varying the conductivity of semiconductors.
- When a crystal is doped such that the equilibrium carrier concentration are different from the intrinsic carrier concentration, the material is said to be extrinsic.

(i) **N-Type Semiconductor**



- If suitable **pentavalent** impurities (antimony, phosphorus and arsenic) are added to intrinsic silicon or Ge, such impurities donate excess (negative) electron carriers, and therefore referred to as donor or n-type impurities.
- This excess electron loosely bound to its parent atom, is relatively free to move within the *n*-type material.
- When impurities are introduced in the material, additional levels are created in the energy band structure, usually within the band gap. For *n*-type impurity, it is called "Donor Level".
- In case of Germanium, donor level is only 0.01 eV (0.05 eV in silicon) below the conduction band, and therefore at room temperature almost all the excess electrons of the donor material are raised into the conduction band.
- N-type semiconductor at 0°K is a perfect insulator.
- Donor energy level (E_D) does not depend upon temperature.
- Energy band diagram of *n*-type semiconductor.

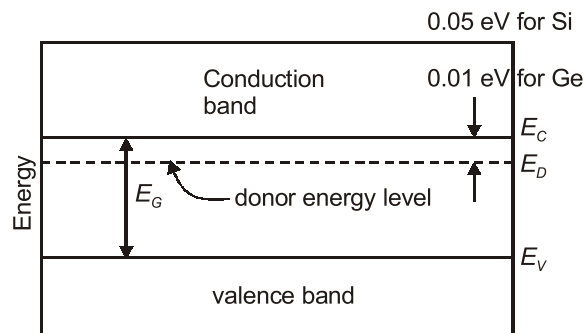
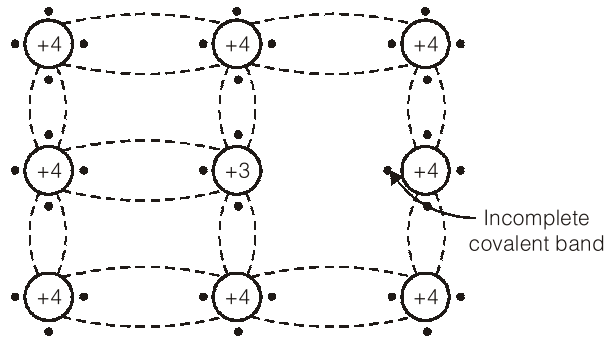


Fig. (a)

- If intrinsic semiconductor is doped with *n* type impurities, not only does the number of electrons increase, but the no. of holes decreases below that which would be available in the intrinsic semiconductor. The reason for the decrease in the no. of holes is that the larger no. of electrons present increases the rate of recombination of electrons with holes.
- In n-type semiconductor, majority carriers are electrons and minority carriers are holes.

(ii) **p-type Semiconductor**

- If **trivalent impurity** (Boron, Aluminium, Gallium or Indium) is added to an intrinsic semiconductor, only three of the covalent bonds can be filled and such impurities make available positive carriers because they create holes which can accept electrons. These impurities are referred to as acceptor or p-type impurities.
- Energy band diagram of *p*-type semiconductor.

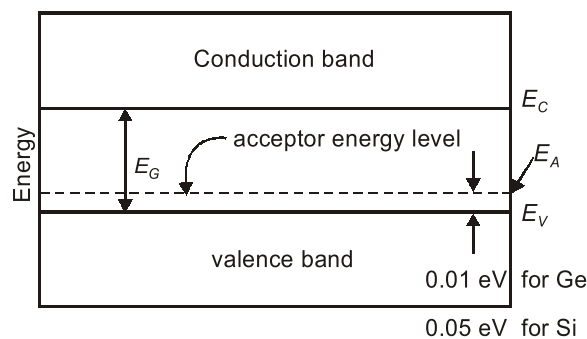


Fig. (b)

- A very small amount of energy is required for an electron to leave the valence band and occupy the acceptor energy level, it follows that holes generated in the valence band by these electrons constitute the largest number of carriers in the semiconductor material.
- The acceptor atom introduces an energy level near the valence band known as Acceptor Energy Level (E_A).
- Acceptor Energy level exist just above the valence band. It represents the energy level of all the trivalent impurities.
- *p*-type semiconductor at 0°K behave as insulator.
- At room temperature, enough thermal energy is available to excite electrons from the valence band to acceptor level.
- Acceptor Energy level (E_A) does not depend upon temperature.
- In *p*-type semiconductor, majority carriers are holes and minority carriers are electrons.

Standard Doping Levels

1. Moderate Doping : 1 in $(10^6 - 10^8)$: P, N
 2. Lightly Doped : 1 in 10^{11} : P⁻, N⁻
 3. Heavily Doped : 1 in 10^3 : P⁺, N⁺
- 1 in 10^6 is read as "1 impurity atom in 10^6 atoms".

Mass-action Law

- Under thermal equilibrium (at constant temperature), the product of positive and negative concentrations is a constant independent of the amount of donor and acceptor impurity doping. i.e.

$$\boxed{np = n_i^2} \quad \dots(1.7)$$

- The intrinsic concentration is a function of temperature.

Charge density in a semiconductor

$N_D \rightarrow$ concentration of donor atoms.

$N_A \rightarrow$ concentration of acceptor atoms.

\therefore semiconductor is electrically neutral

\therefore Total positive charge = Total negative charge

i.e.
$$\boxed{N_D + p = N_A + n} \quad \dots(1.8)$$

\Rightarrow For an n type material. $N_D + p = n$. Because, $n \gg p$

$\therefore \boxed{n \approx N_D \text{ or } n_n \approx N_D}$ 'n' subscript represent 'n' type material

\therefore concentration p_n of holes in n-type will be given by

$$n_n p_n = n_i^2$$

$\therefore \boxed{p_n = \frac{n_i^2}{N_D}} \quad \dots(1.9)$

- For a p type material

$$p_p \approx N_A$$

$\therefore n_p p_p \approx n_i^2$

$\therefore \boxed{n_p \approx \frac{n_i^2}{N_A}} \quad \dots(1.10)$

here 'p' subscript represent 'p' type material.

NOTE:

If concentration of donor atoms added to a p-type semiconductor exceeds the acceptor concentration i.e. ($N_D > N_A$), the specimen changes from p-type to n-type semiconductor. N_D is replaced by ($N_D - N_A$) in above equation (1.9).

1.3 Expression for Minimum Conductivity in Semiconductor

- Conductivity of semiconductor is given as

$$\sigma = nq\mu_n + pq\mu_p$$

- For minimum conductivity

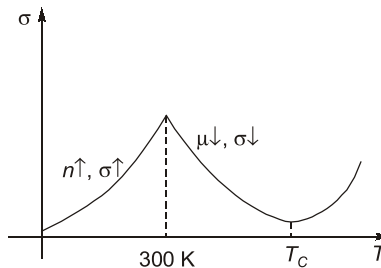
$$n = n_i \sqrt{\frac{\mu_p}{\mu_n}} \quad \text{and} \quad P = n_i \sqrt{\frac{\mu_n}{\mu_p}}$$

- $\sigma_{\min} = 2n_i \sqrt{\mu_n \mu_p} \cdot q$

Effect of Temperature on Conductivity of Intrinsic Semiconductor

- For intrinsic semiconductor, $\sigma = n_i q (\mu_n + \mu_p)$
- As temperature increases, $n_i \uparrow \uparrow$ and $\mu \downarrow \Rightarrow \sigma \uparrow$ as $T \uparrow$
- Conductivity of intrinsic semiconductor increases with temperature.

Effect of Temperature on Conductivity of Extrinsic Semiconductor



$0 < T < 300 \text{ K}$:

Conductivity increases with the temperature. Because of thermal energy, electrons from valence band gets excited to Acceptor level (in p-type semiconductors) and free electrons from Donor Level gets excited to conduction band (in n-type semiconductors).

$300 \text{ K} < T < T_c$:

Majority carrier concentration will remain almost independent of Temperature ($N_D \gg n$ or $N_A \gg P$). As temperature increases, mobility decreases and therefore, conductivity decreases with temperature.

$T > T_c$:

At $T = T_c$ (curie temperature) minority carrier concentration will become equal to majority carrier concentration and the extrinsic semiconductor will become intrinsic semiconductor. Above curie temperature, conductivity increases with temperature.

1.4 Electrical Properties of Ge and Si

Intrinsic concentration

$$n_i^2 = A_0 T^3 e^{-\left(\frac{E_{G_0}}{KT}\right)} \quad \dots(1.11)$$

$E_{G_0} \rightarrow$ energy gap at 0°K in eV

$K \rightarrow$ Boltzman's constant in eV / $^\circ\text{K}$.

$A_0 \rightarrow$ constant independent of temp.

Energy Gap

E_G depends on temperature.

For Si, $E_G(T) = 1.21 - 3.60 \times 10^{-4} T$

at $T = 0^\circ \text{K}$