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ELECTRICAL ENGINEERING

Objective Practice Sets

Microprocessors

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Introduction to 8085 and its Functional Organisation

- Q.1** Microprocessor 8085 is the enhanced version of ____ with essentially the same construction set.
- (a) 6800 (b) 68000
(c) 8080 (d) 8000
- Q.2** The data bus in 8080A / 8085 microprocessor is a group of
- (a) eight bidirectional lines that are used to transfer 8 bits between the microprocessor and its I/O and memory
(b) eight lines used to transfer data among the registers
(c) eight unidirectional lines that are used for I/O devices
(d) sixteen bidirectional lines that are used for data transfer between the microprocessor and memory
- Q.3 Assertion (A):** The development of a microprocessor based product requires the design of program and the hardware.
Reason (R): The design effort for an electronic product follows the same basic steps used in the development of software.
- (a) Both A and R are true and R is the correct explanation of A
(b) Both A and R are true but R is NOT the correct explanation of A
(c) A is true but R is false
(d) A is false but R is true
- Q.4** The output data lines of microprocessors and memories are usually tristated, because
- (a) More than one device can transmit information over the data bus by enabling only one device at a time
(b) More than one device can transmit information over the data bus at the same time
(c) The data lines can be multiplexed for both input and output
(d) It increases the speed of data transfers over the data bus
- Q.5** Machine instructions are written using which of the following?
- (a) Bits 0 and 1 only
(b) Digits 0 to 9 only
(c) Digits 0 to 9 and the capital alphabets A to Z only
(d) Digits 0 to 9, the capital alphabets A to Z and certain special characters
- Q.6 Assertion (A):** Many programmes prefer assembly level programming to machine language programming.
Reason (R): It is possible to efficiently utilise the hardware of the computer in machine language programming.
- (a) Both A and R are true, and R is the correct explanation of A.
(b) Both A and R are true, but R is not a correct explanation of A.
(c) A is true, but R is false.
(d) A is false, but R is true.
- Q.7** Which one of the following statements is correct? A microcontroller differs from a microprocessor in that it has
- (a) both on-chip memory and on-chip ports.
(b) only on-chip memory but not on-chip ports.
(c) only on-chip ports but not on-chip memory.
(d) neither on-chip memory nor on-chip ports.
- Q.8** What is the function of a program counter in an 8-bit microprocessor?
- (a) To store the op-code of the instruction being executed
(b) To store the op-code of the next instruction
(c) To store the address of the instruction being executed
(d) To store the address of the next instruction
- Q.9** When an application is designed using a microcontroller it has the following advantages over a design based on a microprocessor :

1. Its chip count is less.
2. It is more fault tolerant.
3. It is cheaper.

Which of these are correct?

- (a) 1, 2 and 3 (b) 1 and 2 only
(c) 1 and 3 only (d) 2 and 3 only

Q.10 An 'Assembler' in a microprocessor is used for
(a) assembly of processors in a production line
(b) creation of new programs using different modules

- (c) translation of a program from assembly language to machine language
(d) translation of a higher level language into English text

Q.11 What is the direction of control bus?

- (a) Unidirectional into microprocessor
(b) Unidirectional out of microprocessor
(c) Bidirectional
(d) Mixed direction i.e. some lines into microprocessor and some others out of microprocessor

Q.12 Which one of the following statements is correct?
A microprocessor program written in assembly language is translated into machine language. The number of instructions in the machine language when compared with the number of instructions in assembly language is

- (a) More only (b) Same
(c) Less only (d) Either more or less

Q.13 The synchronisation between microprocessor and memory is done by

- (a) ALE signal (b) HOLD signal
(c) READY signal (d) None of these

Q.14 The stack pointer in the 8085 microprocessor is a

- (a) 16 bit register that point to stack memory locations
(b) 16 bit accumulator
(c) memory location in the stack
(d) flag register used for the stack

Q.15 Consider the following registers:

1. Accumulator and B register
2. B and C registers
3. D and E registers
4. H and L registers

Which of these 8-bit registers of 8085 μ P can be paired together to make a 16-bit register?

- (a) 1, 3 and 4 (b) 2, 3 and 4
(c) 1 and 2 (d) 1, 2 and 3

Q.16 In 8085 microprocessor, the value of the most significant bit of the result following the execution of any arithmetic or Boolean instruction is stored in the

- (a) carry status flag
(b) auxiliary carry status flag
(c) sign status flag
(d) zero status flag

Q.17 Statement (I): On executing the HLT instruction, the microprocessor enters into a halt state and all the buses are tri-stated.

Statement (II): On executing the HLT instruction, the microprocessor is disconnected from the system bus till the reset is pressed.

- (a) Both Statement I and Statement II are individually true and Statement II is the correct explanation of Statement I
(b) Both Statement I and Statement II are individually true but Statement II is not the correct explanation of Statement I
(c) Statement I is true but Statement II is false
(d) Statement I is false but Statement II is true

Q.18 Statement (I): In an 8085 microprocessor, an input port and an output port can have same port address.

Statement (II): \overline{RD} is used to enable the input port and \overline{WR} is used to enable the output port.

Q.19 Match **List-I** with **List-II** and select the correct answer using the codes given below the lists:

List-I

- A. Monitor program
B. Assembler
C. Mnemonic
D. Program counter

List-II

1. Used to indicate memory location
2. A combination of letters, symbols and numerals
3. A program that translates symbolic instructions into binary equivalent
4. An operating system

Codes:

- | | A | B | C | D |
|-----|---|---|---|---|
| (a) | 4 | 3 | 2 | 1 |
| (b) | 4 | 3 | 1 | 2 |
| (c) | 3 | 4 | 1 | 2 |
| (d) | 3 | 4 | 2 | 1 |

Q.20 Which of the following statement is false?

1. The 8085 has two 16-bit registers, the PC and SP. The PC is used to sequence the execution of a program and the stack pointer is used as a memory pointer for the stack memory.
2. 8085 responds to four externally initiated operations-reset, interrupt, ready and hold.
3. The interconnection of peripherals with the 8085 MPU, additional logic circuits, called interfacing devices are necessary.

- (a) only 1 is false (b) only 2 is false
(c) only 3 is false (d) None of these

Q.21 In a microprocessor, the service routine for a certain interrupt starts from a fixed location of memory which cannot be externally set, but the interrupt can be delayed or rejected. Such an interrupt is

- (a) non-maskable and non-vector
(b) maskable and non-vector
(c) non-maskable and vector
(d) maskable and vector

Q.22 In a microprocessor when a CPU is interrupted, it

- (a) Stops execution of instructions
(b) Acknowledges interrupt and branches of subroutine
(c) Acknowledges interrupt and continues
(d) Acknowledges interrupt and waits for the next instruction from the interrupting device

Q.23 Match **List-I** with **List-II** and select the correct answer using the codes given below the lists:

List-I

- A. TRAP
B. RST 7.5
C. RST 6.5
D. RST 5.5

List-II

1. Level - sensitive
2. Edge - sensitive
3. Both Level and Edge - sensitive

Codes:

- | | A | B | C | D |
|-----|---|---|---|---|
| (a) | 2 | 3 | 1 | 1 |
| (b) | 3 | 2 | 1 | 1 |
| (c) | 3 | 1 | 2 | 2 |
| (d) | 1 | 3 | 2 | 2 |

Q.24 Consider the following statements regarding RESET instruction of 8085 microprocessor:

1. PC contents become 0000H.
2. All interrupts are enabled.
3. RESET OUT pin is at logic 0.

Which of the above statements is/are correct?

- (a) 1 only (b) 2 only
(c) 1 and 2 (d) 2 and 3

Q.25 Consider the following features in an 8085 microprocessor system with memory mapped I/O:

1. I/O devices have 16-bit addresses.
2. I/O devices are accessed using IN and OUT instructions.
3. There can be maximum of 256 input devices and 256 output devices.
4. Arithmetic and logic operations can be directly performed with the I/O data.

Select the correct answer using the codes given below:

Codes:

- (a) 1, 2 and 4 (b) 1, 3 and 4
(c) 2 and 3 (d) 1 and 4

Q.26 Match **List-I** (Pre terminals) with **List-II** (Applications) and select the correct answer using the code given below the lists:

List-I

- A. SID, SOD
B. Ready
C. TRAP
D. ALE

List-II

1. Wait state
2. Interrupt
3. Serial data transfer
4. Memory or I/O read/write
5. Address latch control

Codes:

- | | A | B | C | D |
|-----|---|---|---|---|
| (a) | 3 | 1 | 5 | 2 |
| (b) | 3 | 1 | 2 | 5 |
| (c) | 4 | 3 | 2 | 5 |
| (d) | 4 | 3 | 1 | 2 |

Q.27 In 8085 microprocessor, the RST 6.5 instruction transfers the program execution to the following location

- (a) 34 H (b) 24 H
(c) 48 H (d) 60 H

- Q.28** Three devices A, B and C are connected to an Intel 8085 A microprocessor. Device A has the highest priority and device C has the lowest priority. The correct assignment of interrupt inputs is
(a) A uses RST 5.5, B uses RST 6.5 and C uses TRAP
(b) A uses RST 5.5, B uses RST 6.5 and C uses RST 7.5
(c) A uses TRAP, B uses RST 6.5 and C uses RST 5.5
(d) A uses TRAP, B uses RST 5.5 and C uses RST 7.5
- Q.29** An 8085 μ p based system drives a multiplexed 5-digits 7-segment display. The digits are refreshed at a rate of 500 Hz. The ON time for each digit is
(a) 4 ms (b) 0.4 ms
(c) 10 ms (d) 25 ms
- Q.30** A memory chip can be represented as 8192×32 . If there are p number of address lines and q number of data lines for the memory chip, then $q-p$ will be equal to _____.
- Q.31** In 8085 microprocessor, RST- n instruction is executed. If the vector address location corresponding to the RST- n instruction is 0038 H, then the value of n is _____.
- Q.32** An 8085 microprocessor is using a crystal frequency of 5 MHz. The duration of one T-state would be _____ ns.
- Q.33** In 8085 microprocessor, the interrupt which is both edge as well as level sensitive has vector address of (_____) ₁₀.



Answers Introduction to 8085 and its Functional Organisation

1. (c) 2. (a) 3. (b) 4. (a) 5. (a) 6. (b) 7. (a) 8. (d) 9. (a)
10. (c) 11. (d) 12. (b) 13. (c) 14. (a) 15. (b) 16. (c) 17. (a) 18. (a)
19. (a) 20. (d) 21. (d) 22. (d) 23. (b) 24. (a) 25. (d) 26. (b) 27. (a)
28. (c) 29. (b) 30. (19) 31. (7) 32. (400) 33. (36)

Explanations Introduction to 8085 and its Functional Organisation

1. (c)

8085 is advanced version of Intel 8080.

2. (a)

Data bus is of 8-bits and bidirectional and transfer data between microprocessor and memory/IO.

3. (b)

Assertion (A): Microprocessor development requires the design of program and hardware.

Reason (R): Electronic product also follows the same basic steps used in software development. Both A and R are correct but R is NOT the correct explanation of A.

4. (a)

The output data lines of microprocessor and memories are tristate because more than one device can transmit information over the data bus by enabling only one device at a time.

5. (a)

A programme written with 0's and 1's is called machine language programme. However sometime to facilitate programmer, machine code can be written in hexadecimal numbers.

6. (b)

Assembly language programmes are written in mnemonics with word like ADD for addition. It is convenient and easy as compared to machine language written in binary codes or in hexadecimal. Machine language is faster as it is the language of microprocessor. It is written in 1's and 0's e.g. in 8085 to add contents of register A and register B, binary code is 10000000. So, time and resources required for conversion of assembly language into machine code is saved. Hence it uses hardware efficiently.